

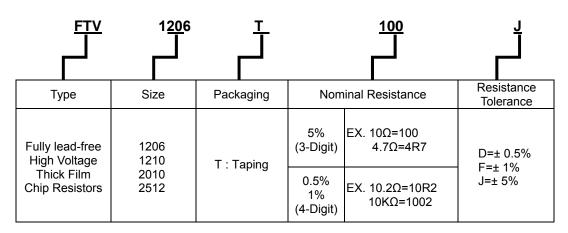
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#### 1 Scope:

- 1.1 This specification is applicable to fully lead-free and halogen-free FTV series thick film chip resistors.
- 1.2 Fully lead-free products without RoHS exemptions
- 1.3 The product is for general electronic purpose.

### 2 Explanation Of Part Numbers:

(EX)



### 3 General Specifications:

	Rated	Max.	Max.	700	Resistance Range		
Туре	Power at 70℃	Working	Overlo ad Voltage	T.C.R (ppm/°C)	D(±0.5%) E-96	F(±1%) E-96	J(±5%) E-24
FTV1206	<u>1</u> _W	500V	1000V	±100	$1\Omega {\le} R {\le} 2.2 M\Omega$	1Ω≦R≦2.2MΩ	$1\Omega {\le} R {\le} 2.2M\Omega$
F 1 V 1200	4	5007	10000	±200			$2.2M\Omega$ <r<math>\leq12M<math>\Omega</math></r<math>
FTV1210	<u>1</u> W	500V	1000V	±100	$10\Omega\!\leq\!R\!\leq\!2.2M\Omega$	10Ω≦R≦2.2MΩ	$1\Omega {\le} R {\le} 2.2 M\Omega$
F I V1210	2 70	3000	10000	±200			$2.2M\Omega$ <r<math>\leq12M<math>\Omega</math></r<math>
FTV2010	<u>3</u> W	2000V	3000V	±100	$1\Omega {\le} R {\le} 2.2 M\Omega$	1Ω≦R≦2.2MΩ	$1\Omega {\le} R {\le} 2.2 M\Omega$
F1 V2010	4	20000	3000	±200			$2.2M\Omega {<} R \! \leq \! 10M\Omega$
FTV2512	1W	3000V	4000\/	±100	$1\Omega \leq R \leq 2.2M\Omega$	1Ω≦R≦2.2MΩ	$1\Omega {\le} R {\le} 2.2M\Omega$
F 1 423 12	100 3	30007	4000V	±200			$2.2M\Omega {<} R \! \leq \! 10M\Omega$
Operat	Operating Temperature Range -55℃~+155℃						

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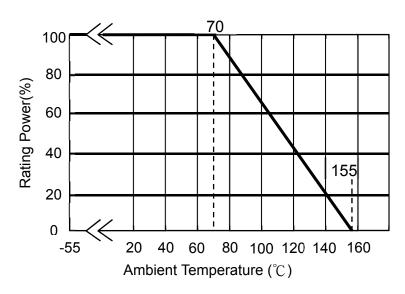


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#### 3.1 Power Derating Curve:

Operating Temperature Range :  $-55{\sim}155~^{\circ}{\rm C}$ 

For resistors operated in ambient temperatures above 70 $^{\circ}$ C, power rating shall be derated in accordance with figure below  $^{\circ}$ 



#### 3.2 Voltage Rating:

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

$$E = \sqrt{R \times P}$$

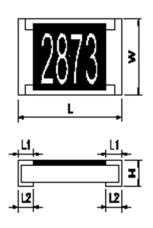
E= Rated voltage (v)

P= Power rating (w)

R= Nominal resistance( $\Omega$ )

### 4 Dimensions:

Unit:mm



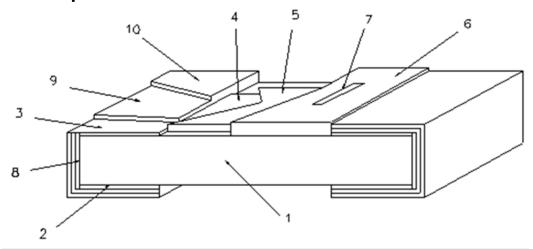
Туре	Dimension Size Code	L	w	н	L1	L2
FTV	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
FTV	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
FTV	2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
FTV	2512	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.69±0.20

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## 5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7 Marking	
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

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## 6 Reliability Test:

## 6.1 Electrical Performance Test

Item	Conditions	Specifications
item	Conditions	Resistors
Temperature Coefficient of Resistance	TCR (ppm/°C) = $\frac{(R2-R1)}{R1 (T2-T1)}$ ×10 <sup>6</sup> R1: Resistance at room temperature R2: Resistance at -55°C or +125°C T1: Room temperature T2: Temperature -55°C or +125°C	Refer to item 3. general specifications
Short Time Overload	Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications)  Refer to JIS-C5201-1 4.13	0.5% 、1%:∆R%=±1.0% 5% : ∆R%=±2.0%
Insulation Resistance	Put the resistor in the fixture, add 100 VDC in + ,- terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material.  Refer to JIS-C5201-1 4.6  Metal block measuring plate insulating plate measuring point B  Metal plate measuring point B  Metal plate measuring point B  Pressurizing by spring	$\geq 10^{9}\Omega$
Dielectric Withstand Voltage	Put the resistor in the fixture, add VAC (see spec. below) in +,-terminal for.  FTV1206 \ 2010 \ 2510 apply 500 VAC 1 minute.  Refer to JIS-C5201-1 4.7	No short or burned on the appearance.

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### 6.2 Mechanical Performance Test

Item	Conditions	Specifications
		Resistors
Terminal Strength	Test1:The resistor mounted on the board applied 5N pushing force on the sample rear for 10 sec.  Test2:The resistor mounted on the board slowly add force on the sample rear until the sample termination is breakdown.  Refer to JIS-C5201-1 4.16	mechanical damage.
Resistance to Solvent	The tested resistor be immersed into isopropyl alcohol of 20~25°C for 5 minutes, then the resistor is left in the room for 48 hrs., and measured its resistance variance rate.  Refer to JIS-C5201-1 4.29	∆R%=±0.5%
Solderability	Preconditioning: Put the tested resistor in the apparatus of PCT, at a temperature of 105°C, humidity of 100% RH, and pressure of 1.22×10 <sup>5</sup> Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more.  Test method: The resistor be immersed into solder pot in temperature 235±5°C for 2 sec, then the resistor is left as placed under microscope to observed its solder area.  Refer to JIS-C5201-1 4.17	Solder coverage over 95%
Resistance to Soldering Heat	for 30 seconds. Then the resistor is left as placed under microscope to observe its solder area.  ©Test method 3 (Electric iron test):	Test item 1:  (1).Variance rate on resistance

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.,		Specifications
Item	Conditions	Resistors
		∆R%=±1.0%
	Preseurtze  OHM Meter  Refer to JIS-C5201-1 4.33	

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### 6.3 Environmental Test

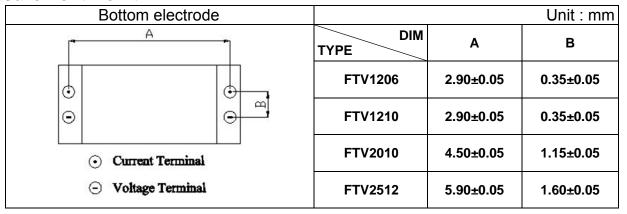
	Conditions	3		Specifications
		Resistors		
' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '		0.5% 、 1%:∆R%=±1.0% 5%:∆R%=±2.0%		
Put the which so	tested resistor in the chamber hown in the following table sha utively. Then leaving the tested	all be repeated 300 ti d resistor in the room	mes	0.5% 、1%:∆R%=±0.5% 5%:∆R%=±1.0%
	Testing Cond	ition		
	Lowest Temperature	-55±5°C		
	Highest Temperature	125±5°C		
	Temperature-retaining time	15 minutes each		
relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.			the	0.5% 、 1%:∆R%=±2.0% 5%:∆R%=±3.0%
Put the tested resistor in chamber under temperature 70±2°C and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.			f, total	0.5% 、1%:∆R%=±2.0% 5%:∆R%=±3.0%
	Refer to Put the which s consecutemperarate.  Refer to Put the which s consecutemperarate.  Refer to Put the relative minutes tested r its resis  Refer to Put the and load 1000 hot temperarate.	Put tested resistor in chamber under 1000 +48/-0 hours. Then leaving the 1000 +48/-0 hours. Then leaving the 1000 tested resistor in the chamber which shown in the following table shown shown in the following the tested temperature for 1 hours, and measure rate.  Testing Cond Lowest Temperature Highest Temperature Temperature-retaining time  Refer to MIL-STD 202 Method 107 Put the tested resistor in the chamber relative humidity 90~95% and load the minutes on, 30 minutes off, total 1000 tested resistor in room temperature for its resistance variance rate.  Refer to JIS-C5201-1 4.24 Put the tested resistor in chamber und and load the rated voltage for 90 minutes and load the rated voltage for 90 minutes temperature for 60 minutes, and mea	1000 +48/-0 hours. Then leaving the tested resistor in roor temperature for 60 minutes, and measure its resistance varate.  Refer to JIS-C5201-1 4.25  Put the tested resistor in the chamber under the Thermal S which shown in the following table shall be repeated 300 ti consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variar rate.  Testing Condition  Lowest Temperature -55±5°C  Highest Temperature 125±5°C  Temperature-retaining time 15 minutes each  Refer to MIL-STD 202 Method 107  Put the tested resistor in the chamber under temperature 4 relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving tested resistor in room temperature for 60 minutes, and me its resistance variance rate.  Refer to JIS-C5201-1 4.24  Put the tested resistor in chamber under temperature 70±2 and load the rated voltage for 90 minutes on, 30 minutes of 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variate.	Put tested resistor in chamber under temperature 155±5°C for 1000 +48/-0 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.  Refer to JIS-C5201-1 4.25  Put the tested resistor in the chamber under the Thermal Shock which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variance rate.  Testing Condition  Lowest Temperature 125±5°C  Highest Temperature 15 minutes each  Refer to MIL-STD 202 Method 107  Put the tested resistor in the chamber under temperature 40±2°C, relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.  Refer to JIS-C5201-1 4.24  Put the tested resistor in chamber under temperature 70±2°C and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.

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#### 7 Measurement Point:



### 8 Plating Thickness:

8.1 Ni: $\geq$ 2 $\mu$ m

8.2 Sn(Tin): $\geq$ 3 $\mu$ m

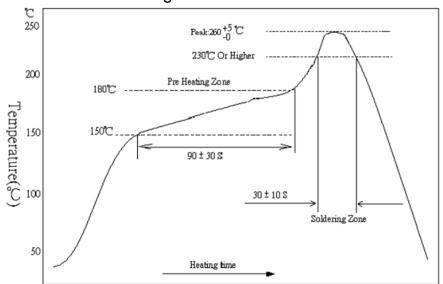
8.3 Sn(Tin): Matte Sn

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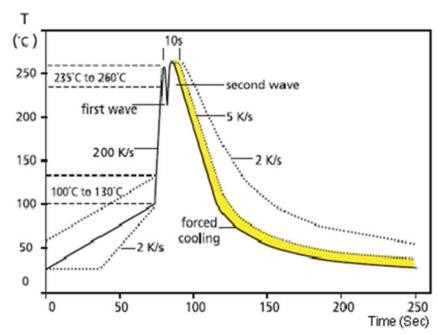
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- 9 Technical application notes: (This is for recommendation, please customer perform adjustment according to actual application)
  - 9.1 Recommend Soldering Method:
    - 9.1.1 Lead Free IR Reflow Soldering Profile



Remark: The peak temperature of soldering heat is 260 +5/-0 °C for 10 seconds

9.1.2 Lead Free Double-Wave Soldering Profile.(This applies to 0603 size inclusive above products)



9.1.3 Soldering Iron: temperature 350°C  $\pm 10$ °C , dwell time shall be less than 3 sec.

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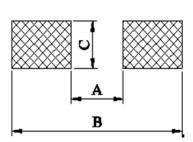


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Unit:mm

#### 9.2 Recommend Land Pattern Design (For Reflow Soldering):

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



			•
TYPE	Α	В	С
FTV1206	2.2	4.2	1.6
FTV1210	2.2	4.2	2.8
FTV2010	3.5	6.1	2.8
FTV2512	3.8	8.0	3.5

#### 9.3 Environment Precautions:

This specification product is for general electronic use, ABCO will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications need to confirm with ABCO.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment
- (b) Exposed to sea breeze or other corrosive gas, such as CI2 \ H2S \ NH3 \ SO2 and NO2.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product.
- (e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

#### 9.4 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

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#### 9.5 Operation and Processing Precautions:

- (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
- (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature.
- (d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
- (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

#### **10 Storage and transportation requirement:**

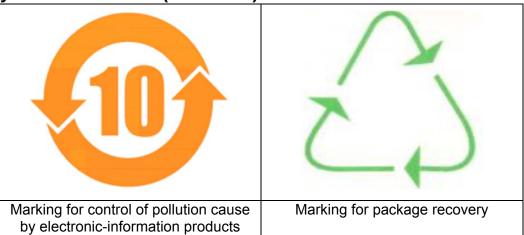
- 10.1 The temperature condition must be controlled at 25±5°C, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years.
- 10.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl2 \ H2S \ NH3 \ SO2 and NO2.
- 10.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

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